

FIG. 1

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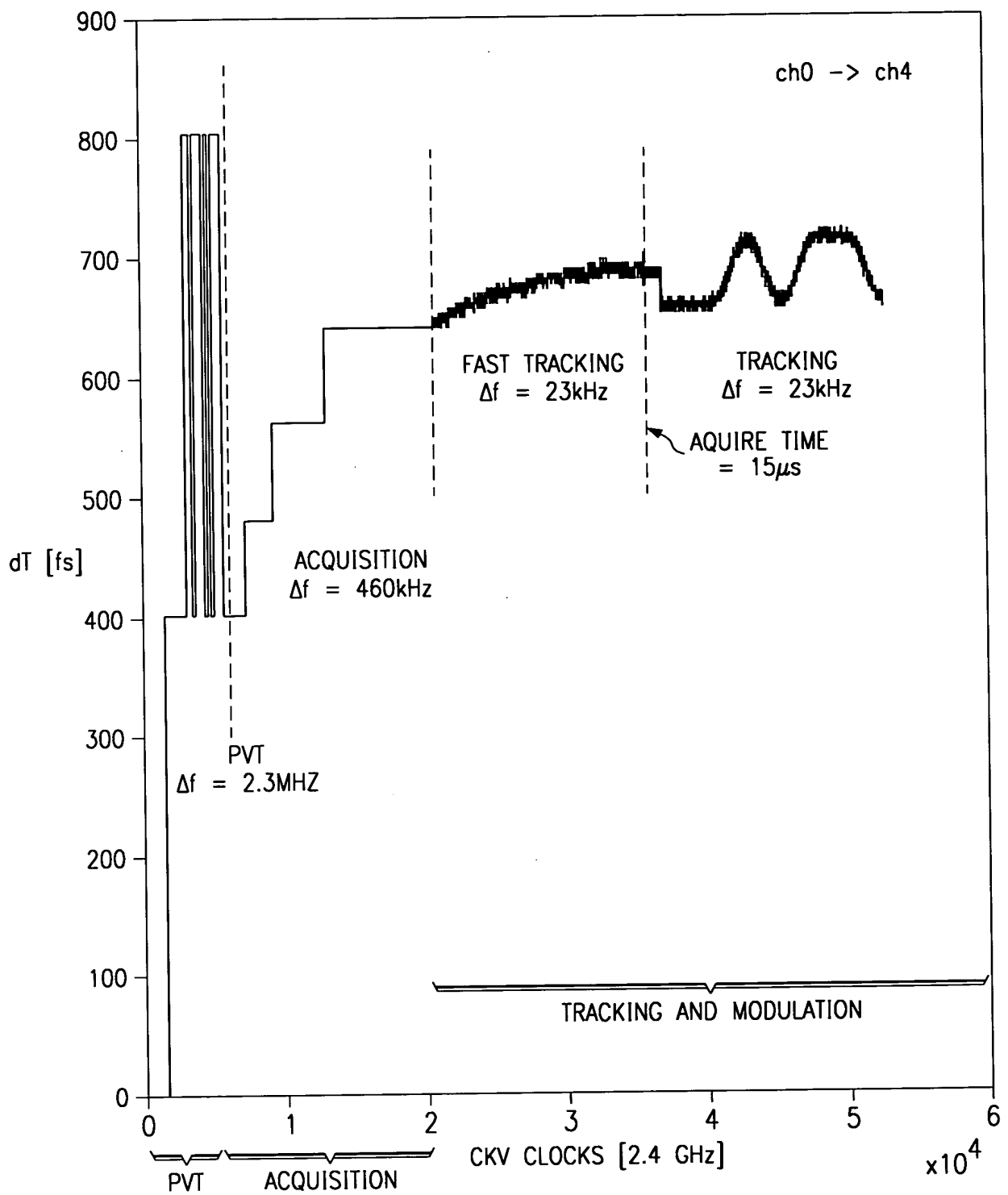


FIG. 2

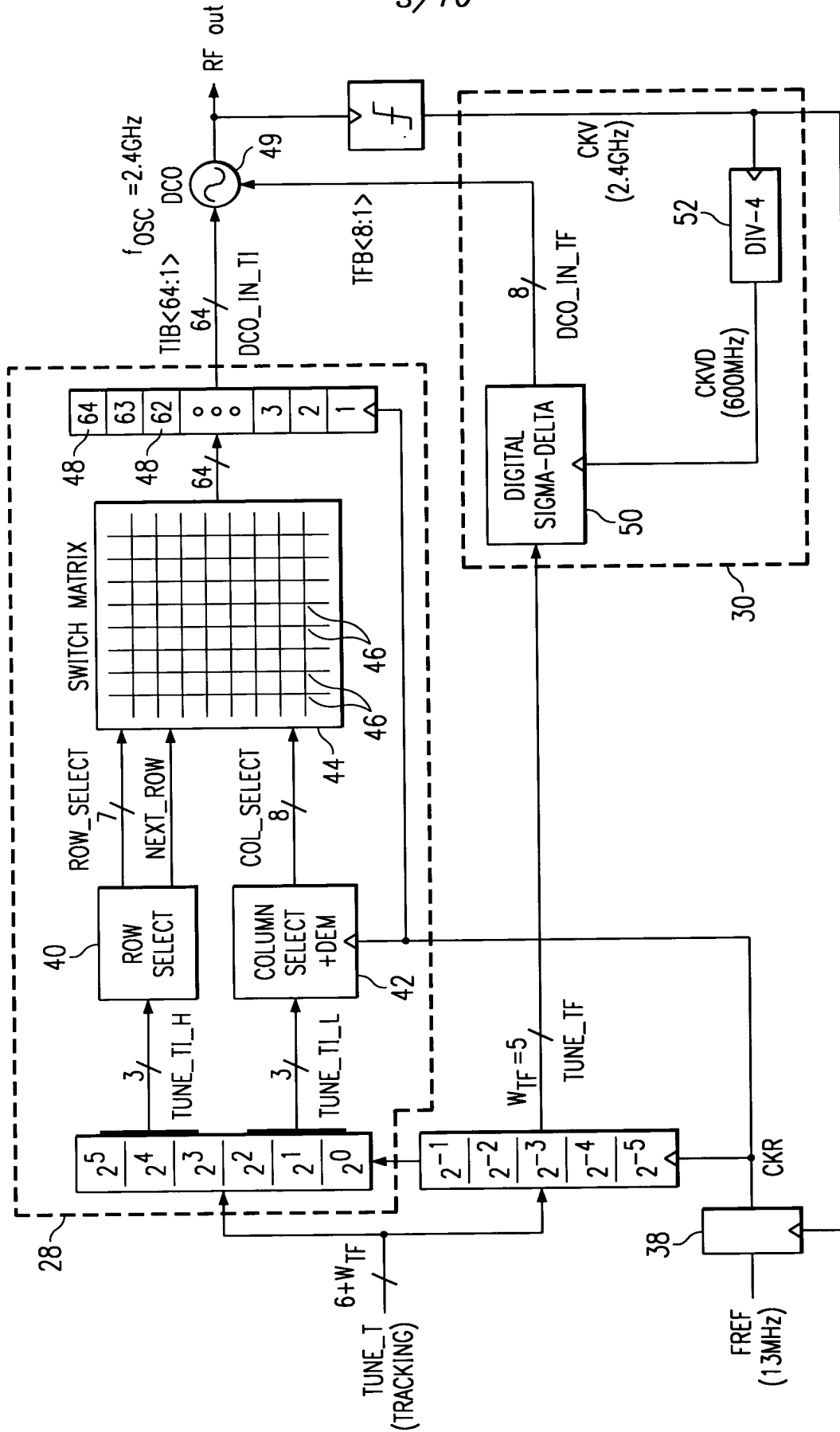
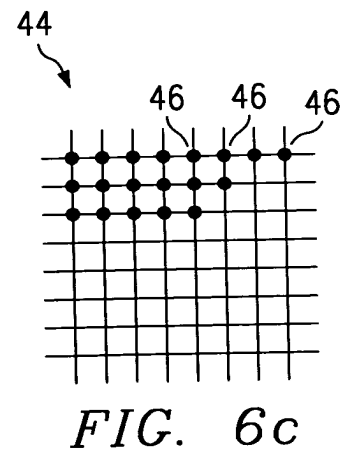
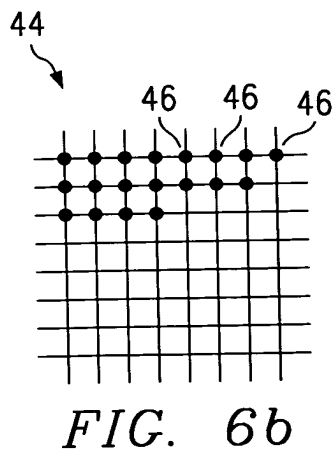
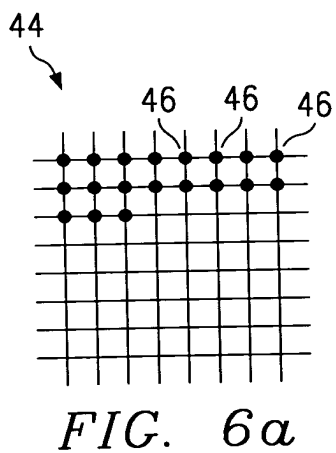
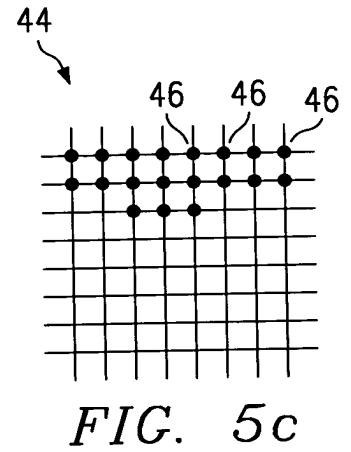
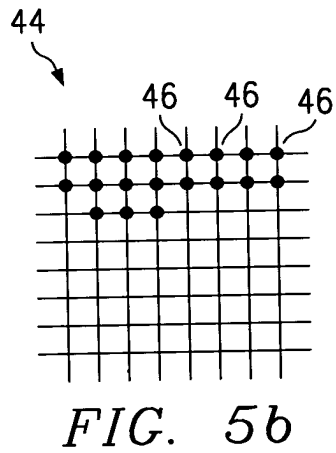
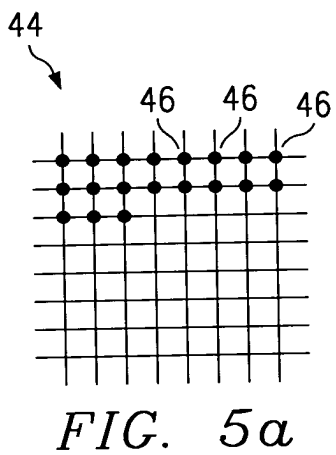
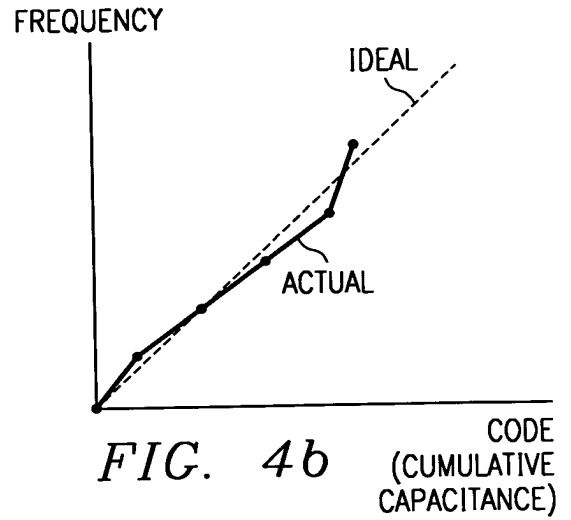
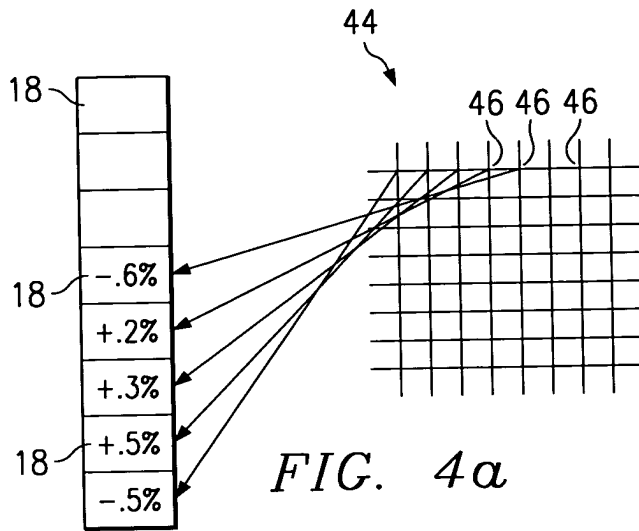


FIG. 3

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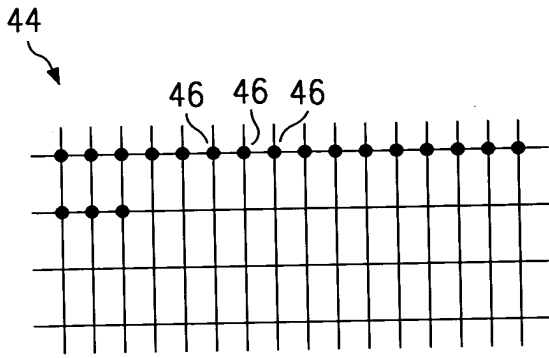


FIG. 7a

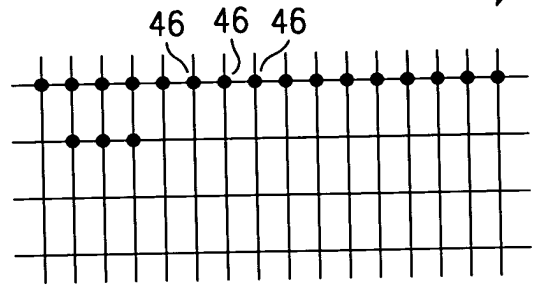
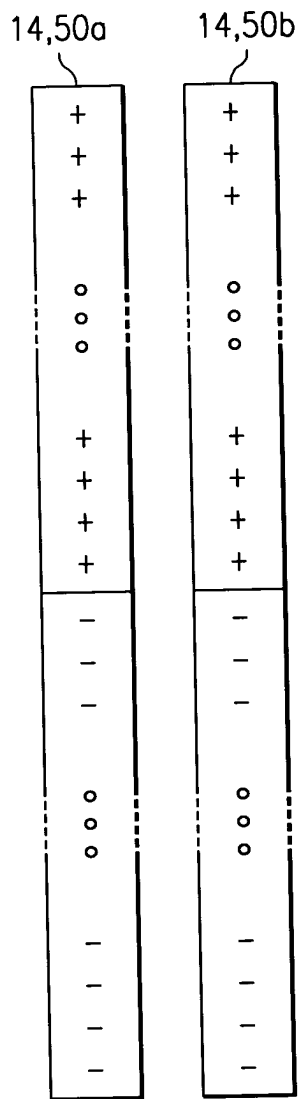
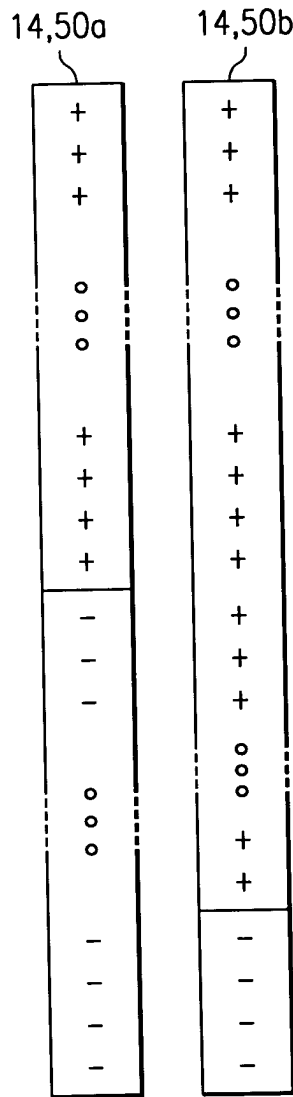


FIG. 7b



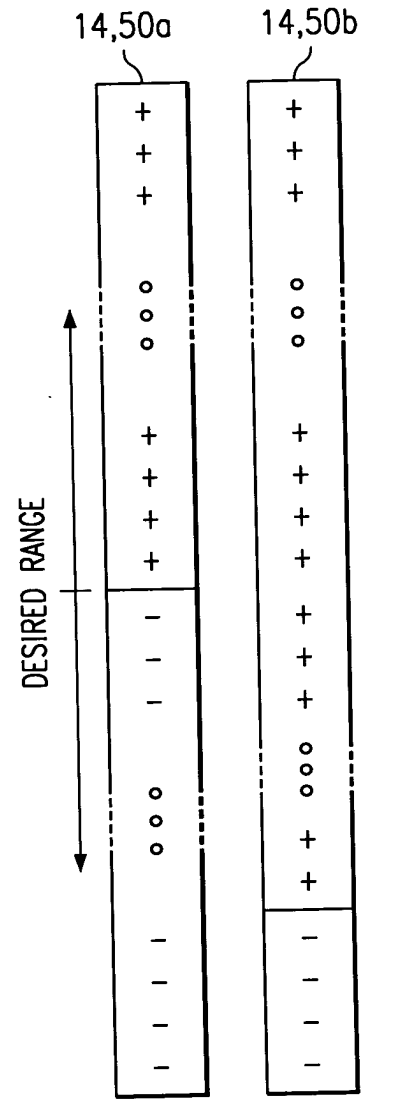
INITIAL STATE

FIG. 9a



CHANNEL TUNING

FIG. 9b



MODULATION AND DRIFT

FIG. 9c

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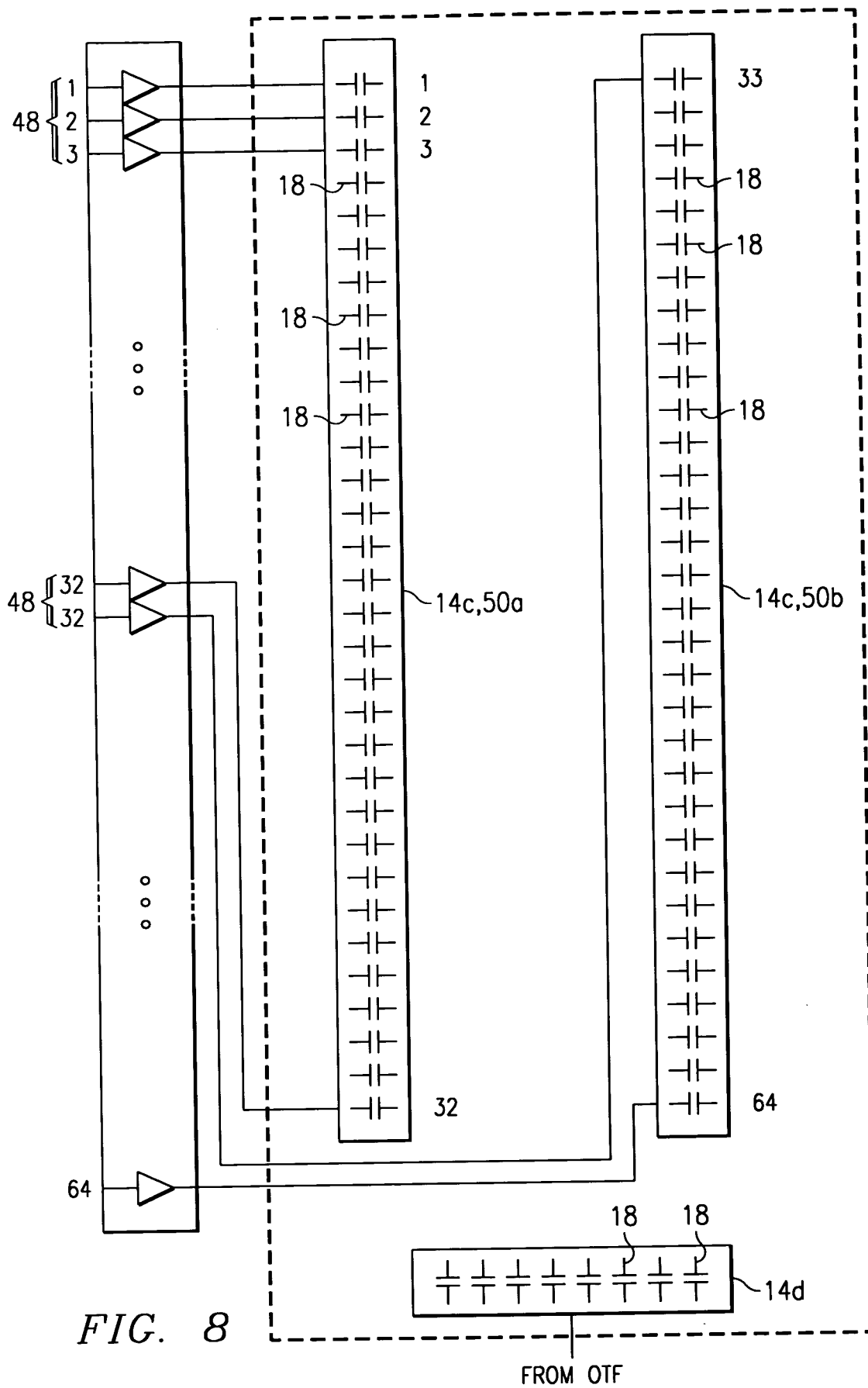


FIG. 8

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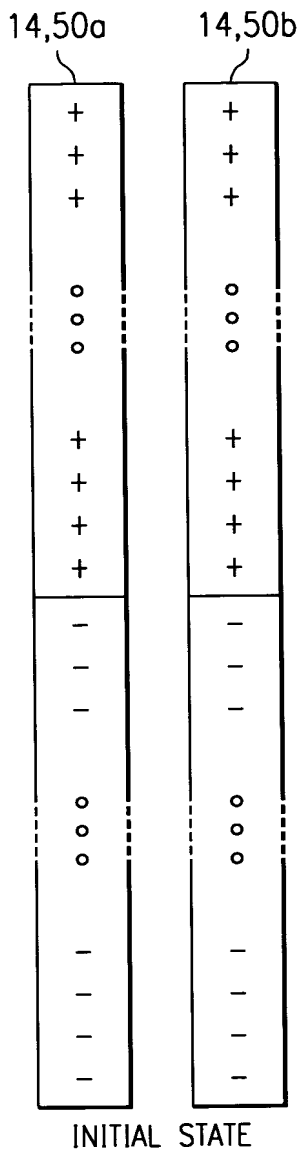


FIG. 10a

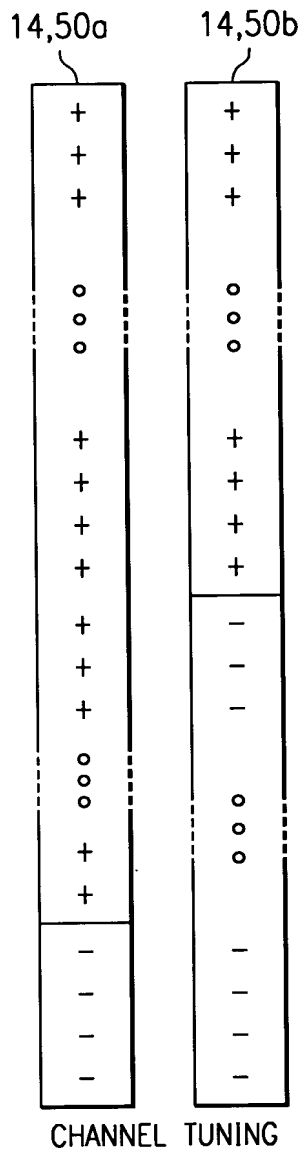


FIG. 10b

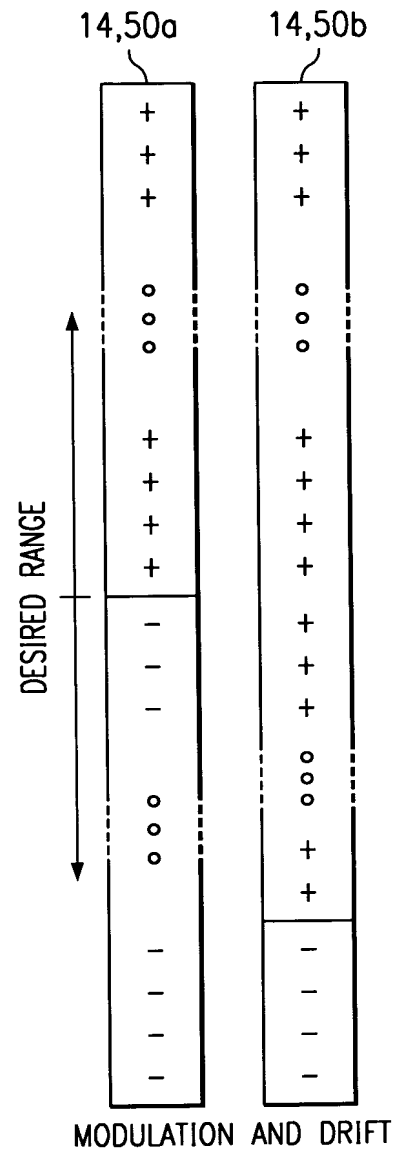


FIG. 10c

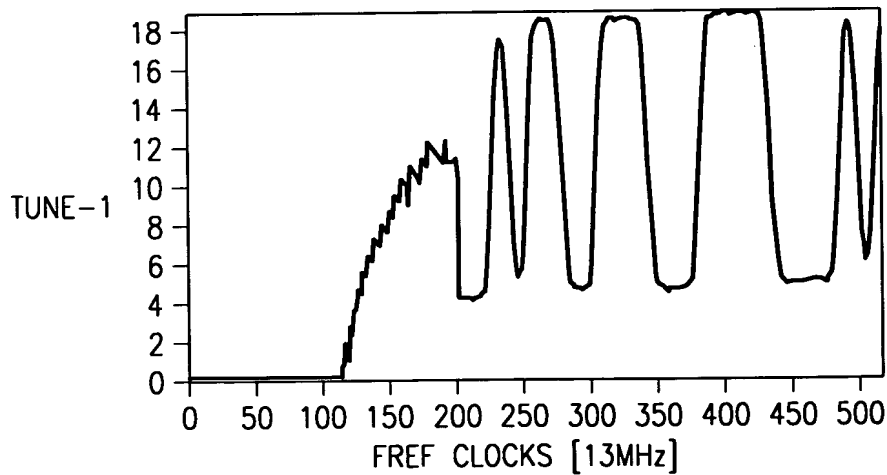


FIG. 11a

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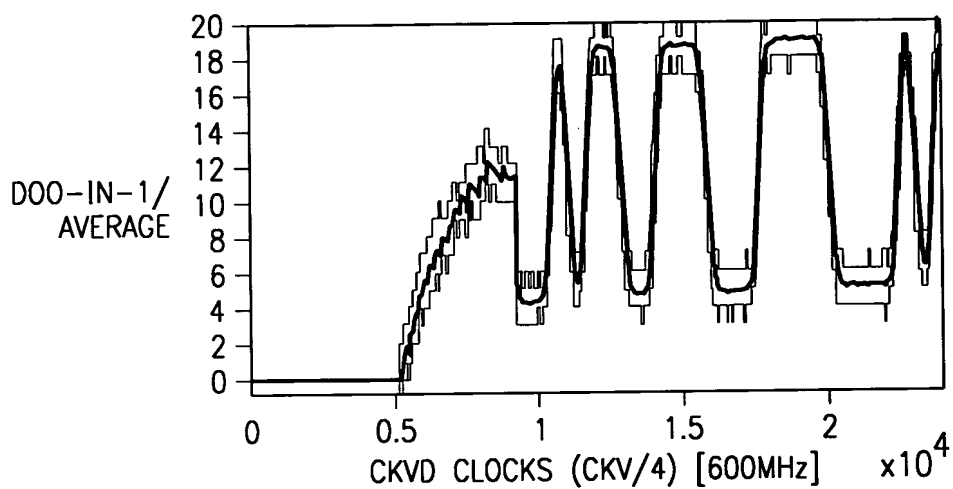


FIG. 11b

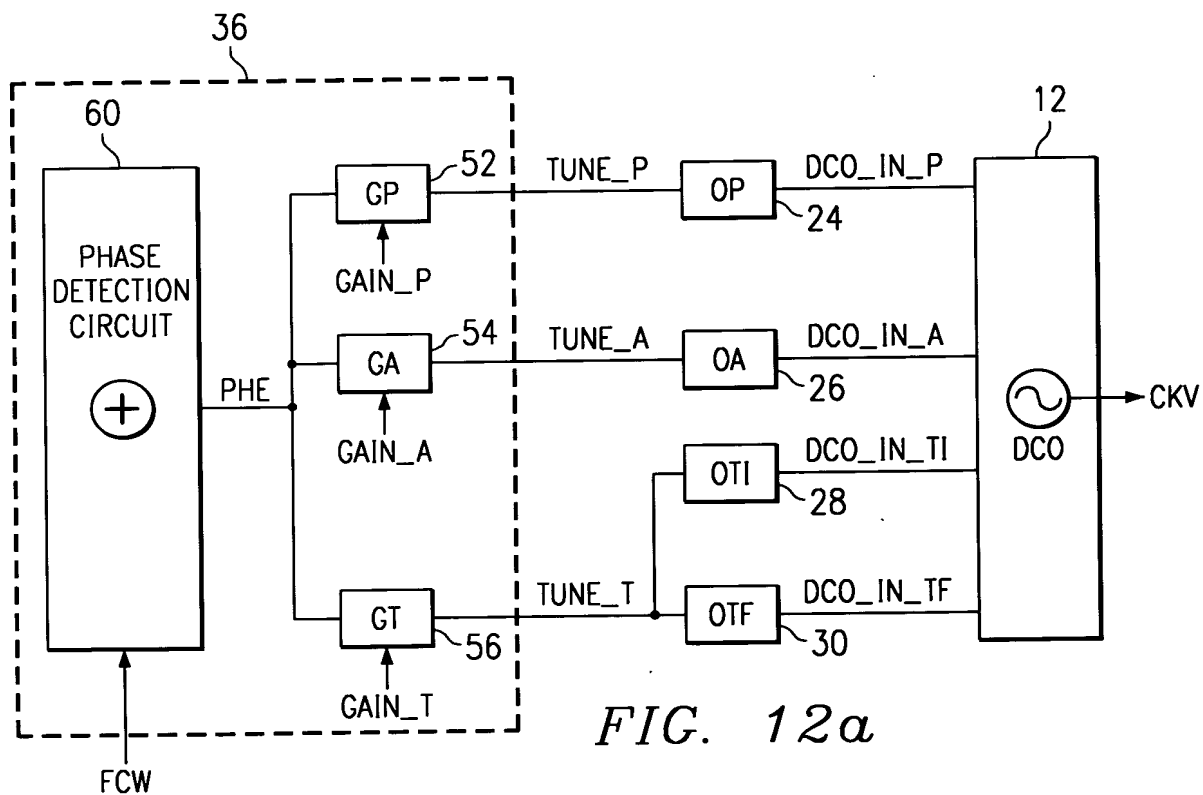


FIG. 12a



